
24C32 - 32-bit Microprocessor (Development)

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The DelSpim is a family of microcontrollers manufactured by Xilinx that use Flash memory. It is based on the Tensilica Xtensa XC8C family, with some different peripherals (e.g., DDR2 / DDR3 memories, PLLs, LCD, power, debug units). Xilinx

has several versions in family (A, B, C,...), each of them having a different number of cores. The DelSpim cores are identified by the core number, for example the 'A' core is actually a core numbered as 'XC8A', etc. New cores will be added in

the future, and for the current family they number up to 'XC8E'. The Xilinx cores may be connected to the same bus, like the XC8C core, but the number of pins on the connector (or the specification of the package) will be different

in each core. The documentation is actually pretty good and includes a table with all the available information about the Xilinx cores. The documentation on the cores and peripheral can be found on the www.xilinx.com web site.

The Xilinx cores are available as a reference implementation for a number of design tools. If you want to use the core, you should know the basics of the following tools: -

FreeSPI is a communication library that allows you to connect all

the cores, it is used from the core level and even beyond. - CABLE is an SPI controller that allow you to easily interface with the Xilinx cores - Java is the environment where the cores and peripherals are programmed. It is used in the cores, but in general

when programming the rest of the system. Xilinx has tutorials that explain how to use the tool. The main difference between the cores is the tool selection, the cores that you can choose from are: - A - B - C - D - CCL - DDR (for DDR2 / DDR3) For each

core, there are related products for different environments that you can choose from: - Softcore - Hardware Programming Framework (IAR Embedded Workbench, Quartus II, ISE) - Java IDE - Validation Suite (XST, VCS). If you are choosing

a hardware platform, you have to choose from the available parts on the market and determine how many cores you need. This may be limited by

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